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PUBLICATIONS

Journal Articles

- S. Patnaik, M. Ashraf, H. Li, J. Knechtel, and O. Sinanoglu (2022). "Concerted Wire Lifting: Enabling Secure and Cost-Effective Split Manufacturing". In: *IEEE TCAD* 41.2, pp. 266–280. ISSN: 1937-4151.
- H. Li, W.-K. Chow, G. Chen, B. Yu, and E. F. Young (2022). "Pin Accessible Legalization for Mixed-Cell-Height Circuits". In: *IEEE TCAD* 41.1, pp. 143–154. ISSN: 1937-4151.
- H. Li, S. Patnaik, M. Ashraf, H. Yang, J. Knechtel, B. Yu, O. Sinanoglu, and E. F. Young (2021). "Deep Learning Analysis for Split Manufactured Layouts with Routing Perturbation". In: *IEEE TCAD* 40.
- G. Chen, C.-W. Pui, H. Li, and E. F. Young (Sept. 2020). "Dr. CU: Detailed Routing by Sparse Grid Graph and Minimum-Area-Captured Path Search". In: *IEEE TCAD* 39.9, pp. 1902–1915. ISSN: 1937-4151.
- H. Li, W. Hong, Y. Liu, and X. Mou (2017). "A novel method of micro-tomography geometric angle calibration with random phantom". In: *Journal of X-Ray Science and Technology* 25.4.

Conference Proceedings

- H. Li, G. Chen, B. Jiang, J. Chen, and E. F. Young (2019). "Dr. CU 2.0: A Scalable Detailed Routing Framework with Correct-by-Construction Design Rule Satisfaction". In: *Proc. ICCAD*. IEEE.
- H. Li, S. Patnaik, A. Sengupta, H. Yang, K. Johann, B. Yu, E. F. Young, and O. Sinanoglu (2019). "Attacking Split Manufacturing from a Deep Learning Perspective". In: *Proc. DAC*. IEEE.
- G. Chen, C.-W. Pui, H. Li, J. Chen, B. Jiang, and E. F. Young (2019). "Detailed Routing by Sparse Grid Graph and Minimum-Area Captured Path Search". In: *Proc. ASPDAC*. ACM, pp. 754–760.
- H. Li, W.-K. Chow, G. Chen, E. F. Young, and B. Yu (2018). "Routability-driven and fence-aware legalization for mixed-cell-height circuits". In: *Proc. DAC*. IEEE.
- C.-W. Pui, P. Tu, H. Li, G. Chen, and E. F. Young (2018). "A Two-Step Search Engine For Large Scale Boolean Matching Under NP₃ Equivalence". In: *Proc. ASPDAC*.
- H. Zhang, F. Zhu, H. Li, E. F. Young, and B. Yu (2017). "Bilinear Lithography Hotspot Detection". In: *Proc. ISPD*. Best Paper Award. ACM, pp. 7–14.

AWARDS

First Place in Initial Detailed Routing

CAD Contest at ISPD

📅 Apr 2019 📍 San Francisco, CA

Second Place in Initial Detailed Routing

CAD Contest at ISPD

📅 Mar 2018 📍 Seaside, CA

First Place in Non-exact Projective NPNP Boolean Matching

CAD Contest at ICCAD

📅 Nov 2016 📍 Austin, TX

EXPERIENCE

Lead Software Engineer

Cadence Design Systems

📅 Jan 2021 – Present 📍 San Jose, CA

Software Engineer Intern

Cadence Design Systems

📅 May 2018 – Sep 2018 📍 Austin, TX

EDUCATION

Ph.D. in Computer Sci. & Erg.

The Chinese University of Hong Kong

📅 Aug 2016 – Jul 2020 📍 Sha Tin, Hong Kong

B.Eng. in Information Engineering

Xi'an Jiaotong University

📅 Sep 2012 – Jun 2016 📍 Xi'an, China

Honors Youth Program

Xi'an Jiaotong University

📅 Aug 2010 – Jun 2012 📍 Suzhou, China

COURSES

EDA for Physical Design of Digital Systems

Online Algorithms for ML & Optimization

Foundations of Optimization

Techniques for Data Mining

Practical Comp. Geometry Algorithms

Hardware for Human Machine Interface

LANGUAGES

Mandarin

English

Cantonese

Japanese



REFEREES

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📍 Texas A&M University